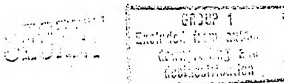


D-R-A-F-T

NPIC/TSSG/PPS  
28 June 1968

QUESTIONS TO BE RESOLVED IN DETERMINING  
A CHIP SYSTEM CONCEPT FOR NPIC

1. What type of chips may be required, or improve the operating efficiency, for the Center, i.e. Interpretation Chips, Data Base Chips, or Reference Chips. For definition of each, refer to Photo Chip Standardization Report?
2. If different type of chips are required or desirable (as referenced above) should they be proportional (or the same size) and be (semi) interchangeable in equipment usage, i.e. a common storage and retrieval concept for all types, or should they all be considered independently?
3. Where will the chips be made (answer for all types)?
4. Who will make the selection or how will the selection be made?
5. Will Target Delineation Chips be useful in making chip selection?
6. How many copies of each will be required by NPIC? By the Community-of-NPIC is responsible for generating.
7. What accountability and/or control techniques are essential? Are desired?
8. What storage and retrieval capability is required? Will it be the same for all types?



Declass Review by NIMA/DOD

SUBJECT: Questions To Be Resolved in Determining a Chip System  
Concept for NPIC

9. Is it more efficient and/or cheaper to mass reproduce multiple copies for dissemination and destruction or retention, or to produce a minimum of copies for close central control and selective loan purposes?
10. What quality standards are required for each type?
11. What handling techniques are best with each given storage and retrieval concept.
12. If chips are received in lieu of multiple copies of roll film, what will our requirements for roll film continue to be? Copies of complete mission coverage? Copies of concentrated target type coverage (selected frame roll printing)?
14. If film is received (in quantity) only in concentrated target coverage, what is the probability of it having to be hand chipped prior to complete exploitation? prior to partial exploitation?
15. If NPIC is responsible for the chip selection for community dissemination, what is the probability of community acceptance, or partial acceptance? Will a Target Delineation Chip in connection with ephemeris or coordinate selection make community acceptance more feasible?
16. How many chips are envisioned in a target delineation file, to be maintained by NPIC as charged in the National Tasking Plan?
17. What is the cost effectiveness and/or operational expediency effectiveness of a full blown chip system as opposed to present reproduction and dissemination procedures.
18. What interrelationships will any chip system concept have with computer capability?
19. What effect will a chip concept have on our Materials Handling concept? On our Information Handling concept?
20. What Materials Handling problems will a chip system concept ease, supplement, or do away with?
21. What Materials Handling problems will a chip system generate?
22. What impact will a Reference Chip system have on our Information Handling concept?